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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application No.: 10780,031 ~~10783,031~~ Confirmation No.: 9275
First Named Inventor: Park, Jongmin Filing Date: 17 February 2004
Group Art Unit: 2824 Examiner: Le, Vu Anh
Atty. Docket No.: R-0003 US
Title: Technique for Programming Floating-gate Transistor Used in
Circuitry Such As Flash EPROM
Assignee(s): Mosel Vitelic Corp.

Mountain View, California
7 July 2005

COMMISSIONER FOR PATENTS
PO Box 1450
Alexandria, Virginia 22313-1450

**INFORMATION DISCLOSURE STATEMENT
UNDER 37 CFR 1.97(b)**

Sir:

Pursuant to 37 CFR 1.56, 1.97, and 1.98, each document listed on the accompanying substitute PTO Form 1449 is called to the attention of the Examiner for the above patent application. Enclosed is a copy of each listed document except for any listed U.S. patent or U.S. patent application publication.

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This information disclosure statement is submitted under the provisions of
37 CFR 1.97(b).

EXPRESS MAIL LABEL NO.:

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Respectfully submitted,

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U.S. Department of Commerce, Patent and Trademark Office				Atty Docket No.		Application No.		
				R-0003 US		10/780,031		
INFORMATION DISCLOSURE STATEMENT BY APPLICANT				First Named Inventor:		Confirmation No.		
Substitute PTO Form 1449				Park, Jongmin		9275		
JUL 07 2005				Filing Date		Group		
				17 February 2004		Unknown		
U.S. Patent Documents								
*Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate	
	AA	5,818,761	10/1998	Onakado et al.	365	185.22		
	AB							
	AC							
	AD							
	AE							
	AF							
	AG							
	AH							
	AI							
	AJ							
	AK							
	AL							
	AM							
	AN							
	AO							
	AP							
Foreign Patent Documents								
							Translation	
		Document	Date	Country	Class	Subclass	Yes	No
	AQ							
	AR							
	AS							
	AT							
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OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)								
	AV							
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<p>*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with your communication to applicant.</p>								

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